

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Jin Kook Jung

Examiner: Benjamin Sandvik

Serial No: 10/822,384

Group Art Unit: 2826

Filed: April 12, 2004

Docket: 8021-224 (SS-19575-US)

For: **SEMICONDUCTOR DEVICE AND METHOD OF LOCATING A  
PREDETERMINED POINT ON THE SEMICONDUCTOR DEVICE**

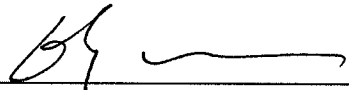
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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTICE OF NON-COMPLIANT**

Sir:

In response to the Notice of Non-Compliant Appeal Brief dated March 5, 2008, Applicant submits herewith corrected section (i.e. pages 1 and 2) of the Appeal Brief submitted on February 22, 2008. Accordingly, please accept the corrected section of the Appeal Brief and withdraw the Notice of Non-Compliant Appeal Brief.

Respectfully submitted,



Frank Chau  
Reg. No.: 34,136  
Jaewoo Park  
Ltd. Rec. No. L0302  
Attorney for Applicants

F. CHAU & ASSOCIATES, LLC  
130 Woodbury Road  
Woodbury, NY 11797  
(516) 692-8888  
(516) 692-8889 (FAX)

**I. INTRODUCTION**

This Appeal is from a Final Office Action mailed on October 11, 2007 finally rejecting claims 1-4, 23 and 24 of the above-identified application. Applicants commenced this Appeal by a Notice of Appeal dated December 11, 2007, which was filed with a Pre-Appeal Brief Request For Review. A Notice of Panel Decision from Pre-Appeal Brief Review dated January 22, 2008 states that the application remains under appeal because there is at least one actual issue for appeal. Accordingly, Applicants hereby submit this Appeal Brief.

**II. REAL PARTY IN INTEREST**

The real party in interest for the above-identified application is Samsung Electronics Co., Ltd., the assignee of the entire right, title and interest in and to the subject application by virtue of an assignment of recorded in the U.S. Patent and Trademark Office at reel 015204 frame 0728.

**III. RELATED APPEALS AND INTERFERENCES**

There are no Appeals or Interferences known to Applicants, Applicants' representatives or the Assignee, which would directly affect or be indirectly affected by or have a bearing on the Board's decision in the pending Appeal.

**IV. STATUS OF CLAIMS**

Claims 1-4, 23 and 24 are pending, stand rejected and are under appeal. Claims 5-22 have been canceled. The claims on appeal are set forth in the attached Appendix.

Claims 1, 23 and 24 are the only independent claims. Claims 2, 3 and 4 are directly depend from claim 1.

**V. STATUS OF AMENDMENTS**

An Amendment and Response to a Non-final Office Action dated June 25, 2007 was filed on September 21, 2007, and was entered.

**VI. SUMMARY OF THE CLAIMED SUBJECT MATTER**

In general, the claimed subject matter relates to marking patterns formed among dummy patterns of a semiconductor device to enable easy location of a point on the semiconductor device.

**A. Embodiments Of Claims 1, 23 and 24**

Claim 1 recites, inter alia, that a number of the dummy patterns is substantially greater than a number of the marking patterns.

For purposes of illustration, the embodiment of claim 1 will be discussed hereafter with reference to Figure 4 and the descriptions in Applicants' specification at page 7, line 5 – page 8, line 17. It is to be understood that the following description of the claimed embodiments and reference to the drawings are for illustrative purposes to provide some context for the claimed embodiments, but nothing herein shall be construed as placing any limitation on the claimed embodiments.

More specifically, by way of example, Applicants recognize that the marking patterns 115 are formed at intervals of 3 dummy patterns (110), however the length of the intervals can be any number of dummy patterns (110) deemed appropriate. Thus, when attempting to locate a desired